# SPECIFICATION FOR LCD MODULE

Model No. <u>TM128160EKFWG1</u>

Prepared by:	Date:
Checked by :	Date:
Verified by :	Date:
Approved by:	Date:

TIANMA MICROELECTRONICS CO., LTD MM. Data Ver 10

## **REVISION RECORD**

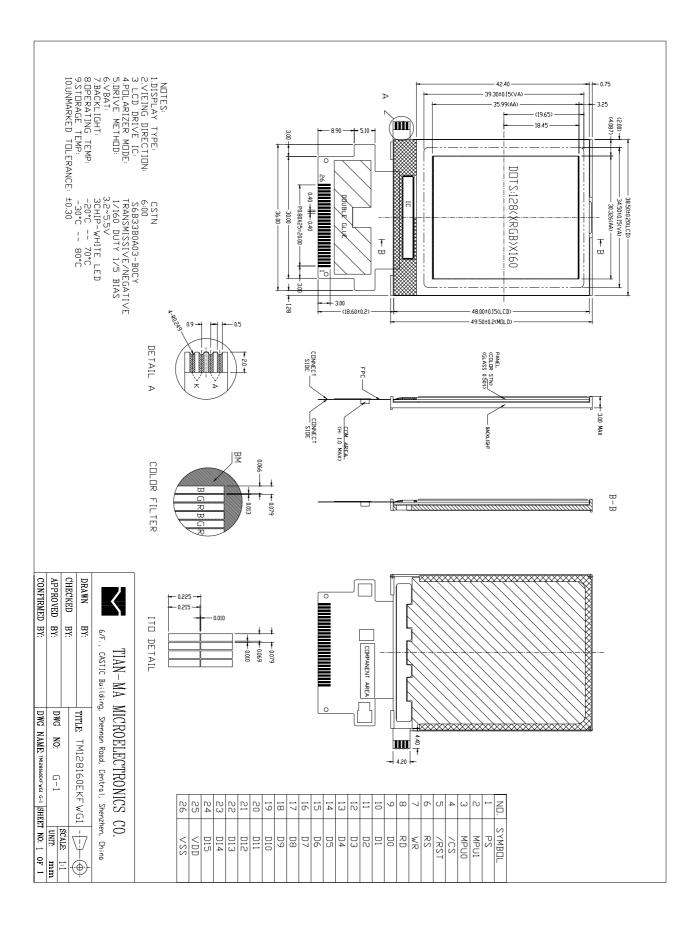
Date	Ver.	Ref. Page	Revision No.	<b>Revision Items</b>

# 1. General Specifications:

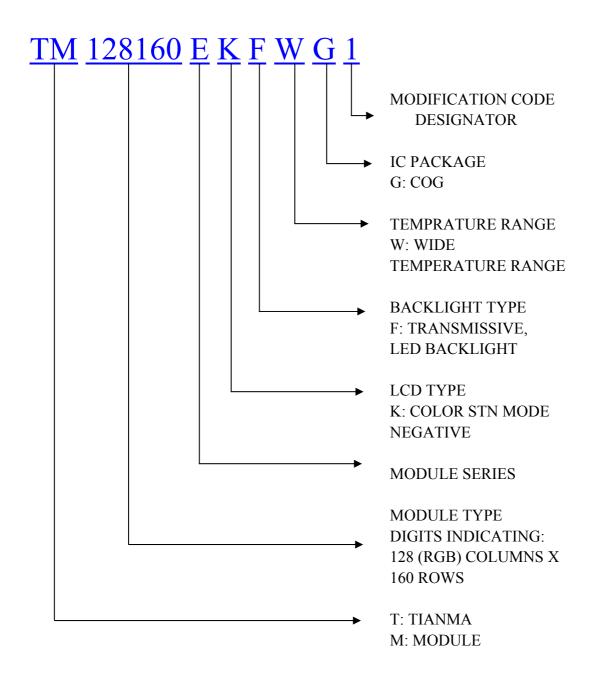
1.1 Display type: COLOR STN
1.2 Display color* <sup>1</sup> :
Display color: 65K COLOR
Background* <sup>2</sup> : Black (Red, Green, Blue dots are off state)
1.3 Polarizer mode: Transmissive/Negative
1.4 Viewing Angle: 6:00
1.5 Driving Method: 1/160 Duty 1/5 Bias
1.6 Backlight Type: LED (3 LAMPS)
Backlight Color: WHITE
1.7 Controller:S6B33B0A03-B0CY
1.8 Data Transfer: 8/16 Bits Parallel or 3/4-PIN Serial Interface
1.9 Operating Temperature: $-20 \sim +70^{\circ}C$
Storage Temperature: $-30 \sim +80^{\circ}$ C
1.10 Power Supply Voltage: VDD=3.0V
1.11 LCD Operating Voltage: VLCD=16.8V
1.12 Outline Dimensions: Refer to outline drawing on next page
1.13 Dot Matrix: $128 \times 3$ (RGB) $\times 160$ Dots
1.14 Dot Size: $0.227(R+G+B) \times 0.215(mm^2)$
1.15 Dot Pitch: $0.237 \times 0.225 \text{ (mm^2)}$
1.16 Weight: $TBD^{*3}$

\*<sup>1</sup> Color tone is slightly changed by temperature and driving voltage.
\*<sup>2</sup> Color tone will be changed by backlight.
\*<sup>3</sup> TBD: To Be Determined.

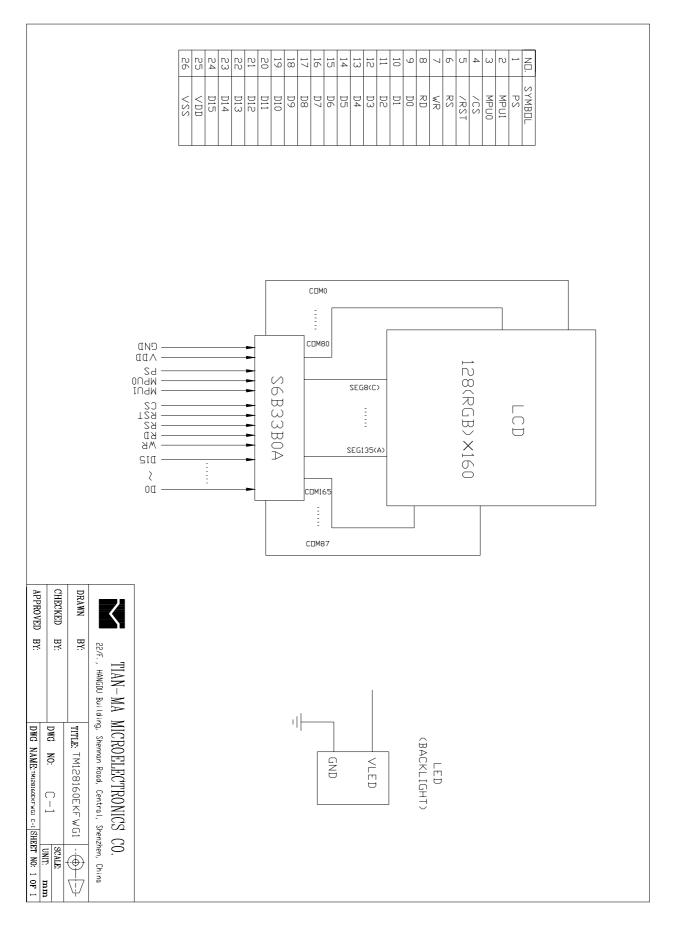
## 2. Outline Drawing



## 3. LCD Module Part Numbering System



## 4. Circuit Block Diagram



# 5. Absolute Maximum Ratings

## Ta=25℃

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	Vdd-Vss	-0.3	+4.6	v	
LCD Driving Voltage	VLCD	-0.3	+20.0	v	
Operating Temperature Range	Тор	-20	+70	°C	No
Storage Temperature Range	Тѕт	-30	+80		Condensation

# 6. Electrical Specifications and Instruction Code

6.1 Electri	ical chara	acteristics		Vss=0V, Ta=25℃			
Iter	n	Symbol	Min.	Тур.	Max.	Unit	
Supply Voltage (Logic)		Vdd-Vss	+3.1	+3.6	+4.5	V	
Supply V (LCD E		Vlcd	-	16.8	-	V	
Input Signal	High	$V_{IH}$ ( $V_{DD}=3.0$ )	$0.8 V_{DD}$	-	$V_{DD}$	V	
Voltage	Low	$V_{IL}$ ( $V_{DD}=3.0$ )	0	-	$0.2V_{DD}$	V	
Supply c (Log		$I_{DD}$ (V <sub>DD</sub> - V <sub>SS</sub> =3.0V)	-	-	2.5	mA	
Oscillator frequency range		$f_{osc}$	220	-	330	KHz	
Supply Voltage (LED)		V <sub>LED</sub>	-	10.0	-	V	
Supply c (LEI		$I_{LED}$		15.0	20.0	mA	

## 6.2 Interface Signals

## 6.2.1 CN1 (FPC)

Pin No.	Symbol	Level		Description					
1	DC	<b>T T</b> / <b>T</b>	PS	MPU1	М	PU0	MPU inter-face select		
1	PS	H/L	Н	L	L		8080-series 8bit interface		
			H	L	Η		8080-series 16bit interface		
2	MPU1	H/L	H H	H H	L H		6800-series 8bit interface 6800-series 16bit interface		
			L	L	X		3 pin SPI(Write only)		
3	MPU0	H/L	L	Н	X		4 pin SPI(Write only)		
4	CS	H/L	Chip	select: L	<b>OW</b>	activ	e		
5	RST	H/L	Rese	t pin: Lo	w a	ctive			
6	RS	H/L	Index	x register	/ [	Data co	ommand select		
7	WR(R/W)	H/L	6800	-series		Read	WRBite control input pin		
/	W K(K/ W)		8080	-series		Writ	e enable clock input pin		
0		TT/T	6800	-series		Read	d / Write control input pin		
8	RD(E)	H/L	8080-series Read enable clock input			enable clock input pin			
9	DB0	H/L							
10	DB1	H/L	-						
11	DB2	H/L	Data	bus bit 0	-7				
12	DB3	H/L				e vali	d in serial interface mode		
13	DB4	H/L	-				ance, DB[6]: serial clock,		
14	DB5	H/L	DB[7	]: serial	dat	a).			
15	DB6	H/L	-						
16	DB7	H/L							
17	DB8	H/L							
18	DB9	H/L							
19	DB10	H/L							
20	DB11	H/L		bus bit 8					
21	DB12	H/L					"low" only in 8-series		
22	DB13	H/L	Paral	parallel interface mode.					
23	DB14	H/L							
24	DB15	H/L							

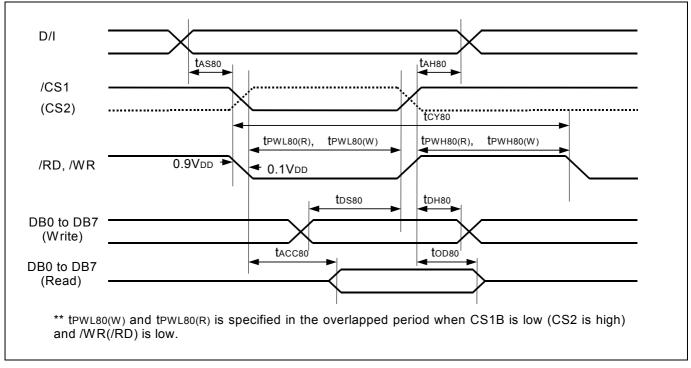
25	VDD	3.0V	Main power supply, 3VDC
26	VSS	0V	Ground

## 6.2.2 CN2 (LED)

Pin No.	Symbol	Level	Description
1, 2	CATHODE	0V	LED CATHODE
3, 4	ANODE	10.0V	LED ANODE

## 6.3 Interface Timing Chart

#### Read / Write Characteristics (8080-series MPU)



#### Parallel Interface (8080-series MPU) Timing Diagram

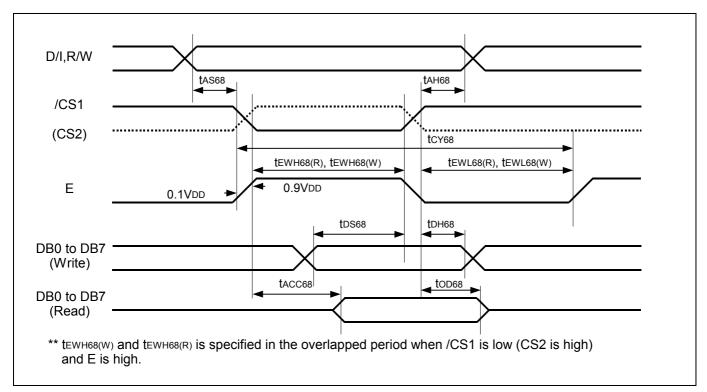
				(VDD	3 = 1.8 to 3.	.3V, Ta = -30 to	) +70°C)
Item	Signal Symbol	Symbol	ol Condition	Min.		Max.	Unit
nem	Signal	Symbol	Condition	3.3V	1.8V	(3.3V/1.8V)	Unit
Address setup time Address hold time	D/I	t <sub>AS80</sub> t <sub>AH80</sub>		0 0	0 0	-	ns
System cycle time		t <sub>CY80</sub>		150	360	-	ns
Pulse width low for write Pulse width High for write	WRB (WRB)	t <sub>PWLW</sub> t <sub>PWHW</sub>		50 30	100 75	-	ns
Pulse width low for read Pulse width high for read	RDB (RDB)	t <sub>PWLR</sub> t <sub>PWHR</sub>		50 30	100 75		ns
Data setup time Data hold time	DB0	t <sub>DS80</sub> t <sub>DH80</sub>		5 8	10 14		ns
Read access time Output disable time	to DB15	t <sub>ACC80</sub> t <sub>OD80</sub>	CL = 100 pF		- tEWHR	60 / 120	ns

#### AC Characteristics (8080-series Parallel Mode)

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NOTE: \*1. The input signal rise time and fall time (tr, tf) is specified at 10 ns or less.

 $(tr + tf) < (t_{CY80} - t_{PWLW} - t_{PWHW})$  for write,  $(tr + tf) < (t_{CY80} - t_{PWLR} - t_{PWHR})$  for read



#### Read / Write Characteristics (6800-series Microprocessor)

Parallel Interface (6800-series MPU) Timing Diagram

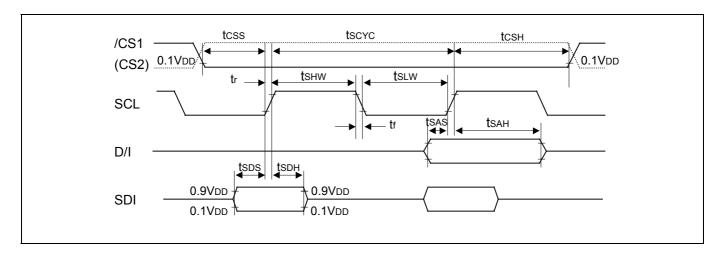
				(VDD	3 = 1.8 to 3.	3V, Ta = -30 to	+70°C)
ltem	Signal Symbol	mbol Condition -	Min.		Max.	Unit	
item	Signal	Symbol	Cymbol Condition	3.3V	1.8V	(3.3V/1.8V)	Onit
Address setup time Address hold time	D/I R/W	tase8 tahe8		0 0	0 0	-	ns
System cycle time		tcy68		150	360	-	ns
Enable width high for write Enable width low for write	RDB (E)	tewhw tewlw		50 30	100 75		ns
Enable width high for read Enable width low for read	RDB (E)	tewhr tewlr		50 30	100 75	-	ns
Data setup time Data hold time	DB0	tds68 tdh68		5 8	10 14	-	ns
Read access time Output disable time	to DB15	TACC68 tod68	C∟= 100 pF		tEWLR	60 / 120	ns

AC Characteristics	(6800-series Parallel Mode)

NOTE: \*1. The input signal rise time and fall time (tr, tf) is specified at 10 ns or less.

(tr + tf) < (tCY68 - tEWHW - tEWLW ) for write, (tr + tf) < (tCY68 - tEWHR - tEWLR ) for read

#### Serial Data Interface Timing



#### Serial Data Interface Timing

	(VDD3 = 1.8 to 3.3V, Ta = -30 to +70°C)					
ltem	Signal	Symbol	Condition	Min.	Max.	Unit
SCL Cycle Time	SCL	tcsc		50	-	ns
SCL High Pulse Width	SCL	tsнw		20	-	ns
SCL Low Pulse Width	SCL	ts∟w		20	-	ns
SDI Setup time	SDI	tsds		20	-	ns
SDI Hold time	SDI	tsdн		20	-	ns
D/I Setup time	D/I	tsas		20	-	ns
D/I Hold time	D/I	tsaн		20	-	ns
Chip Select Setup time	CS1B(CS2)	tcss		20	-	ns
Chip Select Hold time	CS1B(CS2)	tснs		20	-	ns

## 6.4 Instruction code

Instruction Table

Instruction Table														
Instruction Name	D/I	WRB	RDB	DB15 ~DB8	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Hex.	Parameter
Non Operation	0	0	1	*	0	0	0	0	0	0	0	0	00	
Oscillation Mode Set	0	0	1	*	0	0	0	0	0	0	1	0	02	1Byte
Driver Output Mode Set	0	0	1	*	0	0	0	1	0	0	0	0	10	1Byte
DC-DC Select	0	0	1	*	0	0	1	0	0	0	0	0	20	1Byte
Bias Set	0	0	1	*	0	0	1	0	0	0	1	0	22	1Byte
DCDC Clock Division Set	0	0	1	*	0	0	1	0	0	1	0	0	24	1Byte
DCDC and AMP ON/OFF set	0	0	1	*	0	0	1	0	0	1	1	0	26	1Byte
Temperature Compensation Set	0	0	1	*	0	0	1	0	1	0	0	0	28	1Byte
Contrast Control(1)	0	0	1	*	0	0	1	0	1	0	1	0	2A	1Byte
Contrast Control(2)	0	0	1	*	0	0	1	0	1	0	1	1	2B	1Byte
Standby Mode OFF	0	0	1	*	0	0	1	0	1	1	0	0	2C	-
Standby Mode ON	0	0	1	*	0	0	1	0	1	1	0	1	2D	-
DDRAM Burst Mode OFF	0	0	1	*	0	0	1	0	1	1	1	0	2E	-
DDRAM Burst Mode ON	0	0	1	*	0	0	1	0	1	1	1	1	2F	-
Addressing Mode Set	0	0	1	*	0	0	1	1	0	0	0	0	30	1Byte
ROW Vector Mode Set	0	0	1	*	0	0	1	1	0	0	1	0	32	1Byte
N-line Inversion Set	0	0	1	*	0	0	1	1	0	1	0	0	34	1Byte
Entry Mode Set	0	0	1	*	0	1	0	0	0	0	0	0	40	1Byte
X-address Area Set	0	0	1	*	0	1	0	0	0	0	1	0	42	2Byte
Y-address Area Set	0	0	1	*	0	1	0	0	0	0	1	1	43	2Byte
RAM Skip Area Set	0	0	1	*	0	1	0	0	0	1	0	1	45	1Byte
Display OFF	0	0	1	*	0	1	0	1	0	0	0	0	50	-
Display ON	0	0	1	*	0	1	0	1	0	0	0	1	51	-
Specified Display Pattern Set	0	0	1	*	0	1	0	1	0	0	1	1	53	1Byte
Partial Display Mode Set	0	0	1	*	0	1	0	1	0	1	0	1	55	1Byte
Partial Display Start Line Set	0	0	1	*	0	1	0	1	0	1	1	0	56	1Byte
Partial Display End Line Set	0	0	1	*	0	1	0	1	0	1	1	1	57	1Byte
Area Scroll Mode Set	0	0	1	*	0	1	0	1	1	0	0	1	59	4Byte
Scroll Start Line Set	0	0	1	*	0	1	0	1	1	0	1	0	5A	1Byte
Set Display Data Length	х	Х	Х	*	1	1	1	1	1	1	0	0	FC	1Byte
Display Data Write	1	0	1			1	Displa	ay Data	Write	1	1	I.	-	-
Display Data Read	1	1	0					ay Data					-	-
Status Read	0	1	0	0			S	tatus D	ata Rea	ad			-	-
Test Mode1	0	0	1	*	1	1	1	1	1	1	1	1	FF	-
Test Mode2 Test Mode3	0	0	1	*	1	1	1	1 1	1	1	1	0	FE FD	-
Test Mode4	0	0	1	*	1	1	1	1	1	0	1	1	FD	-
Test Mode5	0	0	1	*	1	1	1	1	1	0	1	0	FA	-
Test Mode6 *: Don't care	0	0	1	*	1	1	1	1	1	0	0	1	F9	-

## \*: Don't care

Parameter: The number of parameter bytes that follows instruction data.

# 7. Optical Characteristics

7.1 Optical	Characte	ristics	VLCD	=16.8V	Ta=25°	С		
Iter	n	Symbol	Condition		Min.	Тур.	Max.	Unit
		θx	$\theta_y = 0^\circ$		-45 +30			Deg
Viewing	Angle	θγ	Cr≥2	$\theta_x = 0^{\circ}$	-5(	-50 +50		
Contrast Ratio		Cr	$\theta_x = 0^{\circ}$ $\theta_y = 0^{\circ}$		-	30	-	
Response	Turn on	Ton $\theta_x = 0^{\circ}$			-	-	300	
Time	Turn off	Toff	θ,	√=0°	-	-	200	ms
	Red	X	θχ	≤=0°	-	0.53	-	cd/m <sup>2</sup>
Color	Keu	у	θ,	∠=0°	-	0.37	-	
Of CIE	Crean	X	θχ	$x=0^{\circ}$	-	0.31	-	cd/m <sup>2</sup>
Coord- Inate	Green	у	θ	∠=0°	-	0.51	-	
	Dluc	X	θχ	$\theta_{\rm X}=0^{\circ}$		0.16	-	cd/m <sup>2</sup>
	Blue	у	$\theta_y = 0^{\circ}$		-	0.18	-	

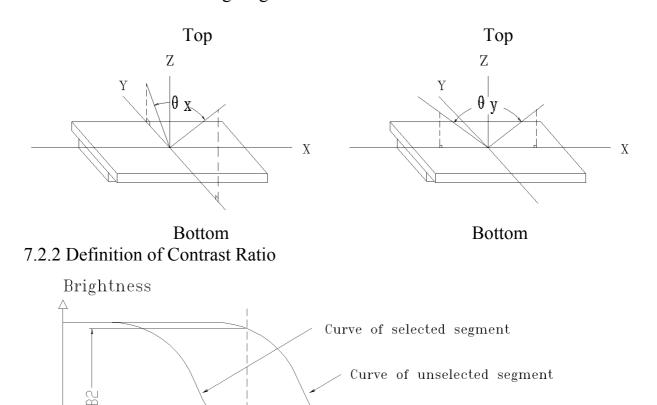
## 7.2 Definition of Optical Characteristics 7.2.1 Definition of Viewing Angle

1  $\widetilde{\mathbf{D}}$ 

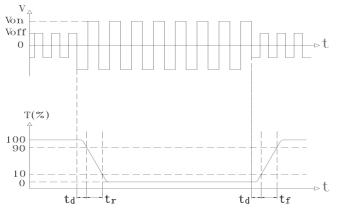
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V o

Contrast Ratio =  $B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$ 



Measuring Conditions: 1) Ambient Temperature:  $25^{\circ}$ C; 2) Frame frequency: 70.0Hz 7.2.3 Definition of Response time



Turn on time:  $t_{on} = t_d + t_r$ Turn off time:  $t_{off} = t_d + t_f$ Measuring Condition:

1) Operating Voltage: 16.8V 2) Frame frequency: 70.0Hz

 $\triangleright$  V

selected state brightness

## 7.3 Brightness Characteristic

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Brightness	Bp	Ta=25℃±3℃	100	150	-	cd/m <sup>2</sup>
Uniformity	$\triangle Bp$	30-80%RH	70	-	-	%

Note:

- 1. The data is measured after LEDs are turned on for 5 minutes.
- 2. Testing conditions LED:  $V_{\text{LED}} = 10.0 \text{ V} (\text{DC})$

LCD: All dots are on (White color)

- 3. Brightness in the center of the LCD panel.
- 4. Definition of Uniformity (△Bp)
  △Bp = Bp (Min.) / Bp (Max.) ×100 (%)
  Bp (Max.) = Maximum brightness in 9 measurement spots
  Bp (Min.) = Minimum brightness in 9 measurement spots

## 8. Reliability

8.1 0	Content of Reliability	v Test	Ta=25℃
No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	$80^{\circ}C \pm 2^{\circ}C 240H$ Restore 4H at 25°C
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-30°C ±2°C 240H Restore 4H at 25°C
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	$70^{\circ}C \pm 2^{\circ}C 240H$ Restore 4H at 25 °C
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	$-20^{\circ}C \pm 2^{\circ}C$ Restore 4H at 25°C
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	70℃±2℃ 90%RH 240H Restore 4H at 25℃
6	Temperature Cycle	Endurance test applying the low and high temperature cycle $-30^{\circ}C \longrightarrow 25^{\circ}C \longrightarrow 80^{\circ}C \longrightarrow 25^{\circ}C$ 30min 5min 30min 5min $\leftarrow 1$ cycle	-30°C/80°C 10 cycles Restore 4H at 25°C
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 100m/s <sup>2</sup> , 120min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 300m/s <sup>2</sup> , 18ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	25kPa 16H Restore 2H

## 8.2 Failure Judgment Criterion

Criterion			T	est	Iter	n N	0.			Failura Indoment Criterian
Item	1	2	3	4	5	6	7	8	9	Failure Judgment Criterion
Basic Specification	$\checkmark$							$\checkmark$	$\checkmark$	Out of the basic Specification
Electrical specification										Out of the electrical specification
Mechanical Specification								$\checkmark$		Out of the mechanical specification
Optical Characteristic									$\checkmark$	Out of the optical specification
Note	For test item refer to 8.1									
Remark	emark Basic specification = Optical specification + Mechanical specification									

# 9. Quality Level

Examination	At T <sub>a</sub> =25°C	Inspection							
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL			
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See	e Append	lix A	II	Major 1.0 Minor 2.5			
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See	e Append	lix B	II	Major 1.0 Minor 2.5			
Minor d	Note: Major defects: Open segment or common, Short, Serious damages, Leakage Minor defects: Others Sampling standard conforms to GB2828								

## **10. Precautions for Use of LCD Modules**

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :0 °C ~ 40 °CRelatively humidity: $\leq 80\%$ 

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

## Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria				
Leakage		Not permitted	Not permitted			
Rainbow		According to the limit specimen				
	Wrong polarizer attachment	Not permitted	l			
Polarizer	Bubble between	Not counted		Max. 3 defects al	lowed	
	polarizer and glass	φ<0.3mm		0.3mm≤¢≤0.51	mm	
	Scratches of polarizer	According to the limit specimen		nit specimen		
Black spot		Not counted	Max	. 3 spots allowed		
(in viewing area)		X<0.2mm	$0.2mm \leqslant X \leqslant 0.5mm$		Max. 3	
	α	X=(a+b)/2			spots (lines)	
Black line (in viewing		Not counted	Max	. 3 lines allowed	allowed	
area)	b b	a<0.02mm	0.021	mm≤a≤0.05mm b≤2.0mm		
Progressive cracks		Not permitted				

## Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents				Criteria		
	Cracks on pads	a	b	I	с	Max. 2	
		≤3mm	$\leqslant$ V	V/5	≤T/2	cracks allowed	
		≤2mm	$\leq V$	V/5	T/2 <c<t< td=""><td>anowed</td></c<t<>	anowed	
	Cracks on contact side	a			b		
		≤3m	m		$\leq T/2$		
		$\leq 2mm$ T/2 <b<t< td=""><td></td><td>Max. 5</td></b<t<>				Max. 5	
Glass		C shall b area	e not	reac	h the seal	Max. 2 cracks	cracks allowed
Cracks	Cracks on non-contact side	a		b		allowed	
		≤3m	m	≪T/2		-	
		≤2mm		T/2 <b<t< td=""><td></td><td></td></b<t<>			
		C≤0.5m	nm				
		d≤SW/3	3				
	Corner cracks	e<2.0mn				Max. 3	
	f-	f<2.0mm	n <sup>2</sup>			cracks	
	e-						

## Appendix B

## Inspection items and criteria for display defects

Items		Contents	Criteria					
Open segmen	Open segment or open common		Not permitted					
Short	Short		Not permitted					
Wrong viewi	Wrong viewing angle		Not permitted					
Contrast radi	o unever	1	According to	the limit specimen				
Crosstalk			According to	the limit specimen				
	-		Not counted	Max.3 dots allowed				
			X<0.1mm	0.1mm≤X≤0.2mm				
Pin holes		X=(a+b)/2	Max.3 dots					
and cracks in segment		→ ►]	Not counted	Max.2 dots allowed	allowed			
(DOT)	H H		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm				
Black spot			Not counted	Max.3 spots allowed				
(in viewing area)			X<0.1mm	0.1mm≪X≪0.2mm				
			X=(a+b)/2	Max.3 spots				
Black line	1		Not counted	Max.3 lines allowed	(lines) allowed			
(in viewing area)			a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm				

## Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria		
		Not counted	Max. 2 defects allowed	
		x<0.1mm	0.1mm≪x≪0.2mm	
		x=(a+b)/2		
				Max.3 defects
	D-+1+1+-a	Not counted	Max. 1 defects allowed	allowed
Transfor- mation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0	
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va	2W alue of width	